



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Johannes Baur et al.  
Serial No. : 10/567,935  
Filed : February 9, 2006  
Title : THIN-LAYER LIGHT-EMITTING DIODE CHIP AND METHOD FOR THE PRODUCTION THEREOF

Art Unit : Unknown  
Examiner : Unknown

**MAIL STOP AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Applicants request consideration of the references listed on the attached PTO-1449 form. Under 37 C.F.R. § 1.98 (a)(2)(ii), only copies of foreign patent documents and/or non-patent literature are enclosed. Copies of any listed U.S. patents or U.S. patent application publications can be provided upon request.

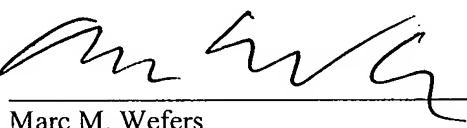
This statement is being filed within three months of the filing date of the application or before the receipt of a first Office Action on the merits. Please apply any charges or credits to Deposit Account No. 06-1050, referencing 12406-148US1.

Respectfully submitted,

Date: \_\_\_\_\_

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CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Substitute Form PTO-1449 (Modified)  Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.95(b))	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 12406-148US1	Application No. 10/567,935
	Applicant Johannes Baur et al.		
	Filing Date February 9, 2006	Group Art Unit	

## U.S. Patent Documents

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	5,310,623	05/10/1994	Gal			
	AB	5,779,924	07/14/1998	Krames et al.			
	AC						
	AD						

## Foreign Patent Documents or Published Foreign Patent Applications

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AE	101 35 190	02/06/2003	Germany	H01L	33/00	Abstract Only	
	AF	1 271 665	01/02/2003	EPO	H01L	33/00		
	AG	1 324 399	07/02/2003	EPO	H01L	33/00		
	AH	1 329 961	07/23/2003	EPO	H01L	33/00		
	AI	WO 01/41225	06/07/2001	WIPO	H01L	33/00		
	AJ							

## Other Documents (include Author, Title, Date, and Place of Publication)

Examiner Initial	Desig. ID	Document
	AK	G.A. Neuman, "Anti-reflective coatings by APCVD using graded index layers", <u>Journal of Non-Crystalline Solids</u> , Vol. 218, pp. 92-99 (1997)
	AL	H.J. Quenzer et al., "Anodic-Bonding on Glass Layers Prepared by Spin-on Glass Process: Preparation Process and Experimental Results", <u>Proceedings of Transducers 01/Eurosensors XV</u> , (June 10-14, 2001)
	AM	I. Schnitzer et al., "30% external quantum efficiency from surface textured, thin-film light-emitting diodes", <u>Applied Physics Letter</u> , Vol. 63, No. 16, pp. 2174-2176 (October 18, 1993)
	AN	S. Warnck "RELIEF -Mass production of low-cost products with microrelief surfaces by means of CD injection molding", <u>Information Series of VDI-VDE-Technologiezentrum Informationstechnik GmbH</u> , (German Federal Ministry for Education and Research) No. 36-2002 (German and English translation)
	AO	R. Windisch et al., "Impact of texture-enhanced transmission on high-efficiency surface-textured light-emitting diodes", <u>Applied Physics Letters</u> , Vol. 79, No. 15, pp. 2315-2317 (October 8, 2001)
	AP	Reducing Reflection by means of Submicron Structures in ORMOCER Layers, Fraunhofer Institute Silicate Research, <a href="http://www.isc.fraunhofer.de/gb/ormocere/o3_7.html">http://www.isc.fraunhofer.de/gb/ormocere/o3_7.html</a>
	AQ	

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	